

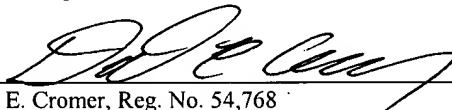


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|---------------------------------|---|---------------------------|
| In re Application |) | PATENT APPLICATION |
| |) | |
| Inventors: Adrian J. Isles |) | |
| |) | Art Unit: 2123 |
| Application No.: 09/586,191 |) | |
| |) | Examiner: Sharon, Ayal I. |
| Filed: June 2, 2000 |) | |
| |) | Customer No. 28554 |
| Title: CIRCUIT-LEVEL MEMORY AND |) | |
| COMBINATIONAL BLOCK MODELING |) | |
| |) | |

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited in the United States Postal Service with sufficient postage as first class mail in an envelope addressed to **Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450**, on August 25, 2004.


David E. Cromer, Reg. No. 54,768
Signature Date: August 25, 2004

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Technology Center 2100

RESPONSE B TO OFFICE ACTION UNDER 37 C.F.R. §1.111

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This **RESPONSE B** is in reply to the Office Action mailed February 25, 2004.

AMENDMENTS TO THE CLAIMS are reflected in the listing of claims which begins on page 2 of this **RESPONSE**.

REMARKS begin on Page 16 of this **RESPONSE**.

This **RESPONSE B** is supported by the Declaration of Adrian J. Isles Under 37 C.F.R. §1.132 ("*Isles Declaration*") and the Declaration of Rajeev Murgai Under 37 C.F.R. § 1.132 ("*Murgai Declaration*"), concurrently filed with this **RESPONSE**.